

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner

Title: Non-volatile transistor memory array incorporating read-only elements with single mask set

Docket No.: 2800.450US1
Filed: March 26, 2004
Examiner: Thinh Nguyen
Customer No.: 76287

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
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We are transmitting herewith the following attached items (as indicated with an "X"):

☒ Amendment and Response under 37 C.F.R. 1.111 (7 pgs.)


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SCHWEGMAN, LUNDBERG & WOESSNER, P.A.
Customer No.: 76287

By: 

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 16 day of January, 2009.


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